

# Quick Start Micro Training LLC

## 2007 IRPS Tutorial – Sample Slides

**2007 International Reliability Physics Symposium Tutorial**

**Sample Slides From INTRODUCTION TO IC RELIABILITY TUTORIAL**


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*Material is from the 3-day Introduction to IC Reliability Course*

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**Speaker's Bio**



Dr. Ted Dellin is the Chief Scientist Emeritus of the Microsystems Center at Sandia National Laboratories having retired from full time work after 35 years at Sandia. He still runs the Microsystems University that he established at Sandia. Dr. Dellin has led the development of the reliability section of the International Technology Roadmap for Semiconductors since the 1990s and is a member of the Sematech Reliability Technical Advisory Board. He is a past chair of the IEEE Nonvolatile Memory Workshop and has given 4 tutorials at the International Reliability Physics Symposium. He has also taught a series of short course in microelectronics and reliability for organizations in the U.S. and Europe. He is a coauthor with Arlene Dellin of the 21<sup>st</sup> Century Semiconductor Technology Handbook and he contributed the Submicron CMOS chapter to the ASM Failure Analysis Desk Reference. Dr. Dellin has a PhD in physics from the City University of New York.

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**Unfortunately for the Pharaoh Cheops The Great Pyramid's Reliability = 0**



- **What was its specified function?**
  - To protect the Pharaoh and his possessions
- **What was the specified environment?**
  - Anything that happened
- **What was the specified lifetime?**
  - Eternity
- **What was the reliability**
  - Tomb broken into in antiquity
  - Reliability,  $R = 0$

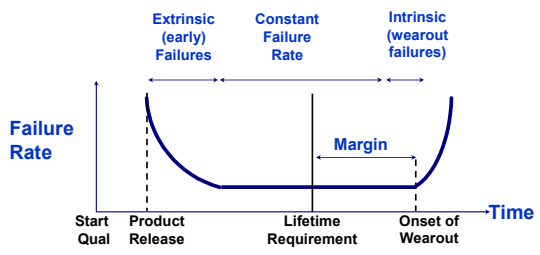
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**Why is Reliability So Challenging?**

- **There are multiple failure mechanisms**
  - Mechanisms change with new materials and new devices
- **For each failure mechanism there is a distribution of lifetimes**
- **Reliability lifetime requirements are too long for real-time testing**
- **Failure rate requirements are so low that a very large number of samples are required**
- **Reliability cannot be independently optimized. It must be traded off against performance, cost, time to market, ...**

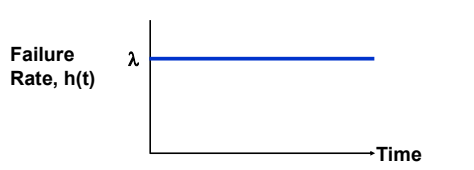
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**Intrinsic and Extrinsic Defects Lead to the "Bathtub Curve"**



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**Exponential Distribution: Failure Rate,  $h(t)$**

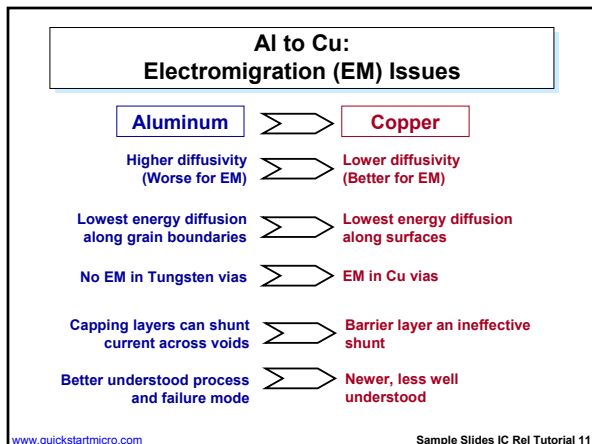
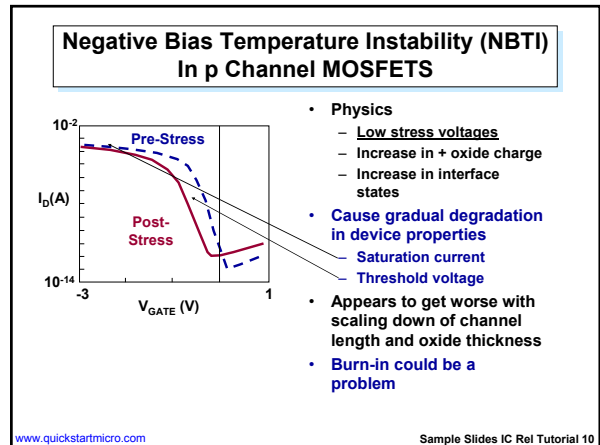
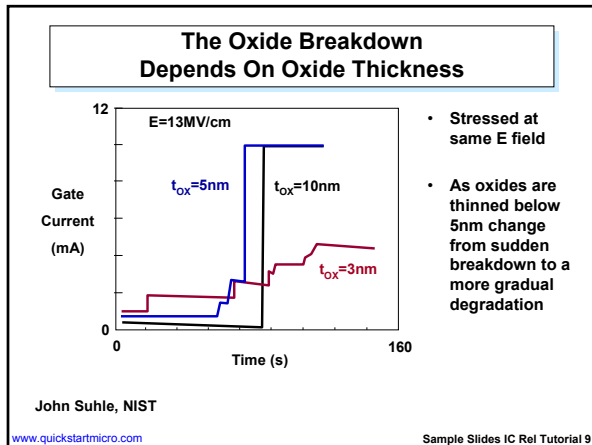
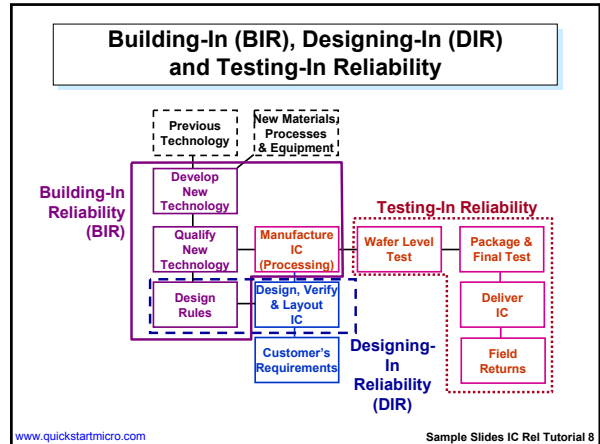
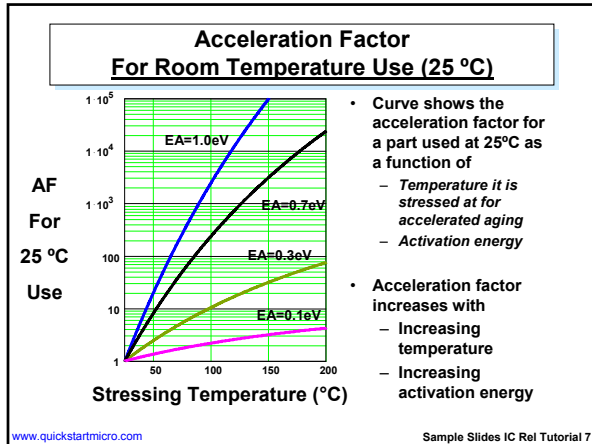


- **Only distribution with a constant failure rate**
- **"Lack of Memory" Instantaneous failure rate the same no matter how long the part has been operated**

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- ### To Learn More
- The complete 3-day Integrated Circuit Reliability Short Course, July 10-12, 2007, Albuquerque**
  - Meeting the Submicron CMOS Challenge, May 16-17, 2007, Albuquerque**
  - Semiconductor Technologies with MEMs, August/September, 2007, Albuquerque,**
  - For more information, including sample slides, visit [www.quickstartmicro.com](http://www.quickstartmicro.com)**
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